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Patent Application

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Date of Deposit:	06/21/00	Signature of the Person Making the Deposit:	<i>Anthony Chou</i>

Inventor(s): Xin Xue

Title: A SYSTEM AND METHOD OF PATCHING MISSING DIGITAL VIDEO PACKETS
COMMUNICATED IN AN IEEE 1394 COMPLIANT IMPLEMENTATION

The Commissioner of Patents and Trademarks
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Sir:

Transmittal of a Patent Application
(Under 37 CFR §1.53)

Transmitted herewith is the above identified patent application, including:

- ☒ Specification, claims and abstract, totaling 35 pages.
☐ Formal drawings, totaling _____ pages.
☒ Informal drawings, totaling 9 pages.
☒ Declaration and Power of Attorney.
☐ Information Disclosure statement.
☐ Form 1449
☒ Assignment(s)
☒ Assignment Recordation Form (duplicate)
☐ Preliminary Amendment
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FEES DUE

The fees due for filing the specification pursuant to 37 C.F.R. § 1.16 and for recording of the Assignment, if any, are determined as follows:

CLAIMS					
	NO. OF CLAIMS		EXTRA CLAIMS	RATE	FEES
Basic Application Fee					\$690.00
Total Claims	20	Minus 20=	0	X \$18 =	\$0.00
Independent Claims	3	Minus 3=	0	X \$78=	\$0.00
If multiple dependent claims are presented, add \$260.00					\$0.00
Add Assignment Recording Fee of \$40.00 If Assignment document is enclosed					\$40.00
TOTAL APPLICATION FEE DUE					\$730.00

PAYMENT OF FEES

The full fee due in connection with this communication is provided as follows:

1. Not enclosed
 - ☐ No filing fee is to be paid at this time.
2. Enclosed
 - ☒ Filing fee
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This application is filed pursuant to 37 C.F.R. § 1.53 in the name of the above-identified Inventor(s).


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Respectfully submitted,

Date: 6-21-2000

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UNITED STATES PATENT APPLICATION FOR

A SYSTEM AND METHOD OF PATCHING MISSING

DIGITAL VIDEO PACKETS COMMUNICATED IN AN IEEE 1394

COMPLIANT IMPLEMENTATION

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CONFIDENTIAL

A SYSTEM AND METHOD OF PATCHING MISSING
DIGITAL VIDEO PACKETS COMMUNICATED IN AN IEEE 1394
COMPLIANT IMPLEMENTATION

5

FIELD OF THE INVENTION

The present invention relates to the field of a digital video (DV)
10 systems and methods. More particularly, the present invention relates to a
system and method for patching digital video information lost during
communication packet transmissions in an IEEE 1394 compliant system.

BACKGROUND OF THE INVENTION

15

Electronic systems and circuits have made a significant contribution
towards the advancement of modern society and are utilized in a number of
applications to achieve advantageous results. Numerous electronic
technologies such as digital computers, calculators, audio devices, video
20 equipment, and telephone systems have facilitated increased productivity and
reduced costs in analyzing and communicating data, ideas and trends in most
areas of business, science, education and entertainment. Electronic systems
designed to produce these results often include digital video technologies that
provide images and audio. Usually, quality video presentations without

glitches require sequentially continuous data streams of digital video information.

Video presentations are typically a very efficient way to present significant amounts of information to an audience. For example, videos capture events and occurrences that are presented to people that were not present when the video was taken. Watching and listening to a video of an event or occurrence provides a virtual first hand experience permitting a viewer to form their own impressions about the event or occurrence. The ability to accurately perceive the information is greatly dependent upon the quality of the presentation. Digital video technologies usually have the potential to provide high quality presentations and typically provide significant presentation manipulation capabilities. However, one of the greatest degradations to effective presentation of video information results from glitches or gaps in the streaming of the visual and audio data.

Typically, digital video technologies involve a number of devices arranged in a variety of configurations. Usually, several of the devices have to communicate with each other in order to perform properly. For example, a digital video camera that captures information associated with images and sounds typically communicates the information to a processor for processing and a storage medium for storage. Communications between a digital video camera and other electronic devices usually have to conform to a

communication protocol in order for the devices to understand the communicated information.

CONFIDENTIAL

An organization that specifies standards for the electronics industry is the Institute of Electrical and Electronics Engineers (IEEE). One standard the IEEE has promulgated is the IEEE 1394 communication protocol standard, which primarily specifies conventions for high speed serial system communications. Communications transmitted in accordance with the IEEE 1394 communication protocol standard are often communicated between computer systems, audio and video products, printers, scanner products, hard disks (especially raid arrays) and digital video camera recorders. In particular, the IEEE 1394 communication protocol standard is often utilized in the transmission of information associated with digital video technology. Usually, the video data is transmitted in an IEEE 1394 communication protocol compliant isochronous packet.

It is common for a certain percentage of communication packets to be lost or discarded during transmission from a source to a destination. When digital video data is transmitted in accordance with IEEE 1394 communication protocol standards in isochronous packets, the real time digital video data is incorporated in the data portion of a Common Isochronous Packet (CIP). There are a number of reasons that a communication packet may be lost or discarded such as hardware or processing problems. When a communication

packet is lost or discarded, the underlying data (e.g., video information) being communicated is lost or distorted. If the underlying data carried by a lost or discarded communication packet is digital video information, there is usually a resulting glitch or gap (e.g., chopped frames) in the presentation such as a
5 chopped frame or other distortion to the presentation.

Accordingly, what is required is a system and method that reduces adverse presentation impacts resulting from missing digital video information carried in lost or discarded communications packets configured
10 in accordance with IEEE 1394 communications protocol standards.

SUMMARY OF THE INVENTION

5 The present invention is a system and method that facilitates reducing of presentation glitches in a digital video system. The present invention is a system and method that automatically determines if digital video (DV) data is missing from a stream of DV information. The present invention also replaces or patches missing DV data with appropriate information to reduce the appearance of interruptions in the video (e.g., glitches in presentations). By replacing or patching missing DV data with appropriate information, the present invention reduces adverse presentation impacts resulting from missing digital video information carried in lost or discarded communications packets configured in accordance with IEEE 1394 communications protocol standards.

15 The present invention is a system and method that facilitates the reduction of presentation glitches in a digital video system. The present invention is a system and method that automatically determines if digital video (DV) data is missing from a stream of DV information and replaces or patches missing DV data with appropriate information to reduce the appearance of interruptions in the video (e.g., glitches in presentations). A communication packet carrying application data is received by an application data patching computer system. In one embodiment of the present invention, an IEEE standard 1394 compliant isochronous packet carrying digital video information is received. The application data (e.g., DV

information) is separated from other communication packet protocol data (e.g., IEEE standard 1394 compliant header information). The received application data is analyzed to determine if it conforms to configuration constraints of predetermined application data format requirements. In one
5 exemplary implementation, the initial information included in the application data section of the communication packet is analyzed to determine if it appropriately (e.g., sequentially) follows the information in previously received communication packet in accordance with predefined configuration definitions. If the information does not appropriately (e.g.,
10 sequentially) follow the information in previously received communication packet, a data patch is provided for lost or missing application data.

In one embodiment of the present invention, an IEEE 1394 communication protocol compliant isochronous packet carrying digital video
15 information is received. A DV data information patching system and method of the present invention determines if the digital video information included in a communication frame appropriately follows previously received digital video information. In one exemplary implementation, the present invention DV data information patching system and method utilizes
20 predetermined configuration standards (e.g., a National Television System Committee (NTSC) or Phase Alternating Line (PAL) DV frame format) to extrapolate if DV information is missing. If the digital video information does not appropriately follow previously received information, the present invention patches the missing digital video information with properly
25 configured default information (e.g., system specific codec defaults).

BRIEF DESCRIPTION OF THE DRAWINGS

The accompanying drawings which are incorporated in and form a part of this specification, illustrate embodiments of the invention and together
5 with the description, serve to explain the principles of the invention:

Figure 1A is a block diagram of application data included in one embodiment of the present invention.

10 Figure 1B is a block diagram of communications packets included in one embodiment of the present invention

Figure 1C is a block diagram of a digital video computer system that utilizes the present invention.

15

Figure 2 is a block diagram of one exemplary hierarchical data format utilized by the present invention for transmitting information associated with a digital video frame.

20 Figure 3 is a block diagram illustration showing the transmission order of digital interface (DIF) blocks in a DIF sequence.

Figure 4 is a table illustrating the relationship of bits in one exemplary DIF block identification (ID) section to information associated with a section

type (SCT), a sequence number (seq), a DIF sequence number (Dseq) and a DIF block number (DBN).

Figure 5 includes a DIF block table that illustrates the correlation
5 between the DIF block ID section bits SCT0 through SCT2 and the section type.

Figure 6 is a table illustrating the DIF sequence definition for NTSC.

Figure 7 is a table illustrating the DIF sequence definition for PAL.

10 Figure 8A is a block diagram showing the configuration of an IEEE standard 1394 compliant CIP, one embodiment of a CIP packet included in the present invention.

15 Figure 8B is a block diagram of a data field configured to communicate digital video information comprising a CIP header section and real time digital video data section.

20 Figure 9 is a flow chart of a missing application data patching method, one embodiment of the present invention.

Figure 10 is a flow chart of DV information patching method 1000, one embodiment of the present invention.

DETAILED DESCRIPTION OF THE INVENTION

Reference will now be made in detail to the preferred embodiments of the invention, a system and method of patching missing digital video
5 information, examples of which are illustrated in the accompanying drawings. While the invention will be described in conjunction with the preferred embodiments, it will be understood that they are not intended to limit the invention to these embodiments. On the contrary, the invention is intended to cover alternatives, modifications and equivalents, which may be
10 included within the spirit and scope of the invention as defined by the appended claims. Furthermore, in the following detailed description of the present invention, numerous specific details are set forth in order to provide a thorough understanding of the present invention. However, it will be obvious to one ordinarily skilled in the art that the present invention may be
15 practiced without these specific details. In other instances, well known methods, procedures, components, and circuits have not been described in detail as not to unnecessarily obscure aspects of the current invention.

The present invention is a system and method of reducing adverse
20 affects associated with missing application data (e.g., digital video information). The application data patching system and method of the present invention facilitates a determination of whether application data is missing and inserts a data patch for missing data. In one exemplary implementation, application data (e.g., digital video information) is carried in

communication packets that are lost or discarded during transmission. The present invention separates the application data from other information associated with the communication packet protocol and then analyzes the application data configuration to determine if the application data properly follows previously received application data. In one embodiment of the present invention, the application data patching system and method determines if a segment or block of application data conforms to the ordering constraints of predetermined application data configuration requirements. If the application data under analysis does not properly follow previously received application data, the present invention inserts appropriate default information. In one embodiment of the present invention, the appropriate default information includes proper data configuration (e.g., sequencing) information and null set information. In one exemplary implementation, null set information does not cause the application to perform a specific function but does provide "filler" information that facilitates the reduction of processing problems.

Figure 1A is a block diagram of application data 150. Application data 150 comprises application data blocks 151 through 159. In one embodiment of the present invention, application data 150 includes digital video information. Application data 150 is communicated between a plurality of electronic devices in communication packets. The application data field included in the communications packets is not large enough to accommodate

all of application data 150 in a single communications packet. Figure 1B is a block diagram of communications packets 171 through 173. Communication packet 171 comprises header 181 and application data field 191 which includes application data blocks 151 through 153. Communication packet 172

5 comprises header 182 and application data field 192 which includes application data blocks 154 through 156. Communication packet 173 comprises header 183 and application data field 193 which includes application data blocks 157 through 159.

10 Application data 150 is transmitted to an electronic device in communications packets 171 through 173. The present invention separates the application data in each received communication packet from other information associated with the communication packet protocol. For example, a present invention application data patching system and method
15 receives communication packet 171 and separates header 181 from application data blocks 151 through 153. The application data patching system and method also receives communication packet 173 and separates header 183 from application data blocks 157 through 159. The present invention then analyzes the received application data blocks to determine if they conform to
20 the ordering constraints of predetermined application data configuration requirements. Since application block 157 does not sequentially follow application data block 153 the present invention determines that communications packet 172 is lost and its associated application data

(application data blocks 154 through 156) are missing. The present invention application data patching system and method patches application data default information for missing application data blocks 154 through 156.

5 Figure 1C is a block diagram of digital video computer system 100, one embodiment of a computer system that utilizes the present invention. In general, digital video computer system 100 comprises bus 107, central processor (CPU) 101, random access memory 102, read only memory 103, data storage device 104, display device 105, alphanumeric input device 108, digital
10 video camera 107, and speaker 109. Bus 107 is coupled to CPU 101, random access memory 102, read only memory 103, data storage device 104, display device 105, alphanumeric input device 108, digital video camera 107 and speaker 109.

15 The components of digital video computer system 100 cooperatively operate to perform their designated functions. CPU 101 processes information and instructions. Random access memory 102 stores information and instructions for CPU 101 in an easily assessable format. Read only memory 103 stores static information and instructions for CPU 101. Data
20 storage device 104 (e.g., a magnetic or optical disk and disk drive) provides bulk storage for relatively larger quantities of information and instructions. Display device 105 displays information to a user. Alphanumeric input device 108 includes alphanumeric and function keys for communicating

information and command selections to CPU 101. In one embodiment of the present invention, alphanumeric input device 108 includes a cursor control device for communicating user input information and command selections to CPU 101. Digital video camera 107 provides digital video information to CPU 101. Speaker 109 generates audio sounds. Bus 107 is a path for communicating information.

The components of computer system 100 comprise a variety of interchangeable embodiments. For example, the display device 105 of Figure 1 may be a liquid crystal device, cathode ray tube, or other display device suitable for creating graphic images and alphanumeric characters recognizable to the user. It is also to be appreciated that there are many implementations of the cursor control device included in alphanumeric input device 108, including a trackball, mouse, joystick or a number of other specially adapted cursor directing devices for providing input to direct and/or activate the cursor. In one embodiment of the present invention the cursor control device is coupled to bus 107 directly.

In one embodiment of the present invention, digital video camera 108 is utilized to capture video images and sounds. Digital video camera 108 includes a lens for capturing rays of light being generated by or reflected off of an object and a microphone to capture sound waves. The rays of light are captured by a transparent lens that directs the light onto a photosensitive

material (e.g., a charge coupled device). The light rays alter the
photosensitive material in a manner that generates an electrical charge when
it is struck by the light rays. The electrical charges are processed into electrical
signals. The microphone includes a transducer that generates electrical
5 signals corresponding to sound waves received by the microphone. The
electrical signals from the photosensitive material and audio transducer
represent video information. The video information is communicated to a
storage device (e.g., ROM 103) or presentation device (e.g., display device 105
or speaker 109).

10

In one implementation of the present invention, video information is
configured in a unit referred to as a video frame. In one National Television
System Committee (NTSC) exemplary implementation of the present
invention, a video frame includes 120,000 bytes of interwoven information
15 including image, audio and instruction information. Figure 2 is a block
diagram of one exemplary hierarchical data format of information associated
with a DV frame 205. DV frame 205 comprises ten Digital Interface (DIF)
sequences including DIF sequence 211, DIF sequence 212 and DIF sequence
213. Each DIF sequence comprises a header section, subcode section, VAUX
20 section and audio/video section that are broken down into DIF blocks. For
example, DIF sequence 212 comprises header section 231, subcode section 232,
VAUX section 233 and audio/video section 234 that are broken down into 150
DIF blocks 241 through 248. In one embodiment of the present invention

there are 150 DIF blocks in each DIF sequence and eighty bytes of information in each DIF block. Each DIF block in Figure 2 includes an identification (ID) section and a data section. For example, DIF block 243 includes ID section 251 comprising three bytes of ID data (ID0, ID1 and ID2) and data section 252 comprising 77 bytes of data.

Figure 3 is a block diagram illustration showing the configuration and transmission sequence of DIF blocks included in a DIF sequence in one DV frame 300. The header section of DV frame 300 includes a header DIF block H0 followed by subcode section DIF blocks SC0 and SC1. The VAUX section includes DIF blocks VA0, VA1 and VA2. Audio/video section comprising DIF blocks A0 through A8 and V0 through V134.

In one embodiment of the present invention, an ID segment of a DIF block includes information associated with a section type (SCT), a sequence number (seq), a DIF sequence number (Dseq) and a DIF block number (DBN). Figure 4 is a table illustrating the relationship of bits in one exemplary DIF block ID segment to information associated with a section type (SCT), a sequence number (seq), a DIF sequence number (Dseq) and a DIF block number (DBN). The four least significant bits of DIF block ID section byte ID0 comprises bits identifying a DIF sequence included in a video frame (e.g., bits Seq0, Seq1, Seq2, and Seq3). The four most significant bits of ID data byte ID0 comprise bits that identify a section type (e.g., bits SCT0, SCT1 and SCT2). The

four most significant bits of DIF block ID section byte ID1 comprise bits .
identifying a DIF block within a DIF sequence (e.g., bits Dseq0, Dseq1, Dseq2,
and Dseq3) . The bits of DIF block ID section byte ID2 comprise data block
number bits DBN0 through DBN7. Figure 5 includes a DIF block type table
5 that illustrates the correlation between the section ID bits SCT0 through SCT2
and the section type. Figure 6 is a table illustrating the DIF sequence
definition for a NTSC digital video frame configuration. Figure 7 is a table
illustrating the DIF sequence definition for Phase Alternating Line (PAL)
digital video frame.

10

The data for a video frame is communicated between the components
of digital video camera computer system 100. One example of a video
information transfer is the communication of information from a camcorder
(e.g., digital video camera 107) to a personal computer CPU (e.g., CPU 101). In
15 one embodiment of the present invention, the DV data is copied to a user
buffer in an entire DV frame. The video frame data is communicated in
different communication formats depending upon the implementation. In
one exemplary implementation, digital video frame information is
encapsulated and communicated in the application data portion of an IEEE
20 1394 communications protocol compliant isochronous packet. In one
implementation of the present invention, an IEEE 1394 communications
protocol compliant isochronous packet includes a common isochronous

packet (CIP) comprising 8 header bytes and 480 data bytes. The 480 data bytes are associated with application data for a DV frame.

Figure 8A is a block diagram showing the configuration of an IEEE 1394 communications protocol complaint isochronous packet 800, one embodiment of a communication packet included in the present invention. IEEE 1394 communications protocol complaint isochronous packet 800 comprises a data length section 811, a tag section 812, a channel section 813, a Tcode section 814, a SY section 815, a header cyclic redundancy check (CRC) section 816, data field section 820 and data CRC section 817. Data length section 818 defines the length of the packet. Tag section 812 defines the format of data carried by the isochronous packet. Channel section 813 identifies a communication channel number. Transaction code (Tcode) section 814 defines the transaction code for an isochronous data block. Synchronization (Sy) section 815 defines the synchronization code which is application specific. Header cyclic redundancy check (CRC) section 816 includes information associated with error detection and correction procedures for the packet. Data field section 820 includes a CIP. Data CRC section 817 includes information associated with error detection and correction procedures for the application data in the data field.

In one embodiment of the present invention, a CIP is utilized to communicate digital video (DV) data from one device (e.g., digital video

camera 108) to another device (e.g., CPU 101). Figure 8B is a block diagram of a data field 820 configured to communicate digital video information comprising a CIP header section 827 and real time digital video data section 829. In one embodiment of the present invention, real time digital video data section 820 includes 480 bytes of digital video information. In one exemplary implementation of the present invention, the real time digital video data section 829 is capable of accommodating or carrying a constant number of DIF blocks (e.g., six DIF blocks). Thus, the first DIF block included in each of sequential CIP data fields (e.g., CIP data field 829) falls within an integer number of DIF blocks of one another (e.g., six blocks) . In one exemplary implementation, the order of the DIF blocks that start a CIP data field include one of the following 25 DIF blocks: H0, A0, V5, V11, V16, V22, V28, V33, A3, V50, V56, V61, V67, V73, V78, V84, A6, V95, V101, V106, V12, V118, V123, V129 (See Figure 3). In one exemplary implementation, the present invention utilizes the predetermined DIF block configuration scheme to predict the DIF block identification of the first DIF block expected in the next sequential CIP data field.

If a CIP (e.g., CIP 800) carrying real time digital video information is lost during a transmission, the present invention determines real time digital video data section (e.g., real time digital video data section 829) DIF blocks are missing and patches appropriate data into processed video frame DIF blocks so that presentation glitches are reduced. For example, if the DIF block

identification of a first DIF block in a group of six DIF blocks included in a CIP doesn't match the expected result, the present invention interprets the communication stream to be missing a CIP packet and fills in data to make the data stream smoother. In one embodiment of the present invention, a

5 the DV information included in one CIP packet is referred to as a DV packet. In one exemplary implementation in which DIF blocks are arranged as shown in Figure 3, a DV packet comprises six status DIF blocks (e.g., H0, SC0, SC1, VA0 VA1, and VA2 blocks), or six audio and video DIF blocks (e.g., A0 through V4) or six video DIF blocks (e.g., V5 through V10).

10

In one exemplary implementation, the patch task performed by the present invention fills in appropriate information if a DV packet (e.g., six DIF blocks) is missing. Based on the predetermined design arrangement of DIF blocks for audio, video, header, subcode and VAUX, the present invention

15 includes the following scheme to patch or fill in the missing data for the DV packet. If the missing packet includes the ID portion of a video frame (e.g., an ID segment or section of a DIF block), the present invention fills in the appropriate value for the sequence number bits (e.g., bits seq0, seq1, seq2, and seq4), reserved bit (e.g., bit rsv), DIF sequence number bits (e.g., Dseq0, Dseq1, Dseq2, and Dseq3), DIF block number bits (e.g., bits DBN0 through DBN7), and

20 section type bits (e.g., bits SCT0, SCT1, and SCT2). These appropriate values are calculated to patch or fill in an appropriate sequential value based upon the video frame information included in the previous DV packet.

In one exemplary implementation of the present invention, the first DIF block of a DV packet received immediately before the currently received DV packet includes a DIF sequence number value of 1, DIF block number 0, and section type value of logical 000. The currently received DV packet included a DIF sequence number value of 1, DIF block number 5, and section video type value of logical 100. The present invention determines a DV packet is missing since the proper values for the currently received DV packet should be a DIF sequence number value of 1, DIF block number 0, and section type value of logical 011 (See Figures 3 and 4). In one exemplary implementation, the present invention compares the section type bits (SCT) and DIF block number bits (DBN) of a previously received packet to the SCT bits and DBN bits of a currently received DV packet. The present invention inserts a patch DV packet with the appropriate ID information (e.g., a DIF sequence number value of 1, DIF block number 0, and section type value of logical 011). The present invention patches or fills in a value of logical 1111 for the sequence number and a value of logical 1 for the reserved bit. In one embodiment of the present invention the patch value for the sequence number is based upon codec defaults of a particular system.

The present invention also patches or fills in missing DIF blocks associated with a data portion of a video frame. In one embodiment of the present invention, a hexadecimal value of "FF" is patched or filled in for

video, VAUX0 through VAUX2, H0 and SC0 through SC1. In one exemplary implementation, the present invention fills in five bytes of hexadecimal value "FF" in the audio auxiliary data AAUX field and fills the remaining portion of the audio data field repeatedly with either 0X808080 (12 bit coding) or 0X80808080 (16 bit coding). The choice of either 0X808080 (12 bit coding) or 0X80808080 (16 bit coding) is based on the actual coding setting reflected in the quantization (QU) AAUX field in the A3 DIF block of DIF sequence number 0 when the first byte of AAUX equals 0X50. If the bits of QU are logical value "000" it is sixteen bit coding and if the bits of QU are a logical value "001" it is twelve bit coding.

In one embodiment of the present invention, the software architecture included in digital video camera computer system 100 comprises several layers in which an MS direct show filter sits on top of kernel module layers. The kernel module layers comprise a MS stream class driver, Sony DV mini driver, MS 1394 bus class driver and Sony 1394 port driver. The digital video frame assembly is performed at the SONY DV mini driver layer based upon video information communicated in an IEEE 1394 communications protocol compliant CIP. In one exemplary embodiment, the present invention patches or fills in missing DIF information at the SONY DV mini driver layer. The present invention determines which DIF block is missing and then fills in the ID part and data portion of each missing DIF block.

Figure 9 is a flow chart of missing application data patching method 900, one embodiment of the present invention. Missing application data patching method 900, facilitates the reduction of adverse affects associated with missing or discarded application data carried in communication packets.

5 In one exemplary implementation, missing application data patching method 900, facilitates a determination if application data is lost during a communications transmission and inserts a compatible data patch when appropriate.

10 In step 910, a communication packet carrying application data is received. In one embodiment of the present invention, an IEEE standard 1394 compliant isochronous packet carrying digital video information is received. In one exemplary implementation the digital video information is configured in a NTSC format and in another implementation the
15 information is configured in a PAL format.

In step 920, application data (e.g., DV information) is separated from other communication packet protocol data (e.g., IEEE standard 1394 compliant header information). In one embodiment of the present invention, this
20 includes separating header information from application data field information.

In step 930, received application data (e.g., DV information) is analyzed to determine if it conforms to configuration constraints of predetermined application data format requirements. In one exemplary implementation, the initial information included in the application data section of the communication packet is analyzed to determine if it appropriately (e.g., sequentially) follows the information in previously received communication packet in accordance with predefined configuration definitions. If the information does not appropriately (e.g., sequentially) follow the information in previously received communication packet, DV information patching method 900 interprets the inconsistency to indicate a communication packet and its associated application data is lost or missing.

In step 940, a data patch is provided for lost or missing application data. In one embodiment of the present invention appropriate ordering information and default application data is provided. For example, in one implementation of the present invention, the patch task performed by the present invention fills in appropriate information if a DV packet (e.g., six DIF blocks) is missing. If the missing packet includes the ID portion of a video frame, the present invention fills in the appropriate value for the sequence number bits (e.g., bits seq0, seq1, seq2, and seq4), reserved bit (e.g., bit rsv), DIF sequence number bits (e.g., Dseq0, Dseq1, Dseq2, and Dseq3), DIF block number bits (e.g., bits DBN0 through DBN7), and section type bits (e.g., bits SCT0, SCT1, and SCT2). These appropriate values are calculated to patch or fill in an

appropriate sequential value based upon the video frame information included in the previous DV packet. The present invention also patches or fills in missing DIF blocks associated with a data portion of a video frame. In one embodiment of the present invention, a hexadecimal value of "FF" is

5 patched or filled in for other DIF blocks included in a video frame.

In step 950, application data is forwarded for further processing and DV information patching method 900 returns to step 910. The forwarded application data includes patch data provided in step 940.

10

In one embodiment of present invention, DV information patching method 900 includes a configuration option checking step (not shown). In the configuration option checking step, an application data configuration option is checked to ascertain if application data is configured in accordance

15 with a particular option. In one exemplary implementation, the present invention fills in the first five bytes of hexadecimal value "FF" in an AAUX field and fills in the remainder of the AAUX field with either 808080 or 80808080 repeatedly. The choice of either 808080 or 80808080 is based on an analysis of a coding setting reflected in the QU AAUX field in the A3 DIF

20 block of sequence number 0. If the bits of QU are logical value "000" it is sixteen bit coding and if the bits of QU are a logical value "001" it is twelve bit coding.

Figure 10 is a flow chart of DV information patching method 1000 in one embodiment of the present invention. DV information patching method 1000 facilitates the reduction of adverse affects (e.g., glitches) associated with missing or discarded DV application data carried in communication packets. In one exemplary implementation, DV information patching method 1000 facilitates a determination if DV application data is lost during a communications transmission and inserts a compatible data patch when appropriate.

In step 1010, a communication packet is received. In one embodiment of the present invention, an IEEE standard 13104 compliant common isochronous packet (CIP) carrying digital video information is received. In one exemplary implementation the digital video information is configured in a NTSC format and in another implementation the information is configured in a PAL configuration.

In step 1020, application data (e.g., DV information) is separated from other communication packet (e.g., CIP) protocol data. In one embodiment of the present invention, this includes removing the data length section (e.g., data length section 811), tag section (e.g., tag section 812), channel section (e.g., channel section 213), Tcode section (e.g., Tcode section 814), Sy (e.g., Sy section 815), a header CRC section (e.g., header CRC section 816) and a data CRC section (e.g., data CRC section 817).

In Step 1030, DV information patching method 1000 analyzes if the received communication packet includes a start of a DV frame. In one embodiment of the present invention, DV information patching method 5 1000 determines if a communication packet includes a start of a DV frame by checking to ascertain if the Dseq bits (e.g., Dseq 0,1,2,3) and section type bits (e.g., sct0, sct1 and sct2) and the sequence bits are all logical zeroes (e.g., seq0, seq1, seq2 and seq3). If all these bits are logical zeroes, DV information patching method 1000 considers the received communication packet to 10 include a start of a DV frame.

In Step 1040, a QU value is retrieved from AAUX field in the A3 DIF block of DIF sequence number zero when the first byte of AAUX equals to 0X50, it is an audio section and the DIF block number (DBN) is set to equal 15 0x03.

In Step 1050, a determination is made if an appropriate starting DIF block is received first in a communication packet. In one exemplary implementation, the present invention determines if information associated 20 with a sequence (Seq), a DIF sequence number (Dseq) and a DIF block number (DBN) is sequentially continuous from a previously received communications packet.

In Step 1060, DV information patching method 1000 forwards the received communication packet for further processing if the section (SCT), DIF block number (DBN), and DIF sequence value (Dseq) match expected values and no patch is performed.

5

In Step 1070, DV information patching method 1000 performs a DIF patch if section type (SCT), a DIF sequence number (Dseq) and a DIF block number (DBN) does not match expected values. DV information patching method 1000 patches a DV packet comprising six status DIF blocks (e.g., H0, SC0, SC1, VA0 VA1, and VA2 blocks), or six audio and video DIF blocks (e.g., A0 through V4) or six video DIF blocks (e.g., V5 through V10) based upon the last DV packet information. After a patch is performed DV information patching method 1000 returns to step 1010.

10

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Thus, the system and method of the present invention is a system and method that facilitates the reduction of presentation glitches. The present invention is a system and method that automatically determines if a digital video (DV) packet is missing from a video stream communication. The present invention also replaces missing packets with appropriate fill in information to reduce the appearance of interruptions in the streaming of information (e.g., glitches in presentations).

20

The foregoing descriptions of specific embodiments of the present invention have been presented for purposes of illustration and description. They are not intended to be exhaustive or to limit the invention to the precise forms disclosed, and obviously many modifications and variations are possible in light of the above teaching. The embodiments were chosen and described in order to best explain the principles of the invention and its practical application, to thereby enable others skilled in the art to best utilize the invention and various embodiments with various modifications as are suited to the particular use contemplated. It is intended that the scope of the invention be defined by the Claims appended hereto and their equivalents.

CLAIMS

What is claimed is:

- 5 1 A digital video computer system comprising:
- a bus for communicating information;
- a digital video camera for capturing video information associated with
- an image and sound, said digital video camera coupled to said
- communications bus;
- 10 a processor for patching missing video information, said processor
- coupled to said communications bus;
- a monitor for displaying said image, said monitor coupled to said
- communications bus; and
- a speaker for transmitting said sound, said speaker coupled to said
- 15 communications bus.
- 2 The digital video computer system of Claim 1 wherein said processor
- determines if said video information is missing.
- 20 3 The digital video computer system of Claim 1 wherein said video
- information is encapsulated in a communications packet compliant with
- Institute of Electrical and Electronics Engineers (IEEE) 1394 communication
- protocol standard.

4 The digital video computer system of Claim 3 wherein said processor is
also for separating said video information from other information associated
with said communication packet and analyzing the configuration of said
5 video information to determine if said video information properly follows
previously received video information.

5 The digital video computer system of Claim 4 wherein said processor is
also for inserting appropriate default information if said video information
10 under analysis does not properly follow previously received video
information.

6 The digital video computer system of Claim 1 wherein said processor is
also for determining if a segment or block of said video information
15 conforms to the ordering constraints of predetermined video information
configuration requirements.

7 A digital video computer system comprising:
a means for communicating information;
20 a means for capturing video information associated with an image and
a sound, said means for capturing video information coupled to said means
for communicating information;

a means for processing information to determine if said video information is not received and providing patch video information, said means for processing information and patching missing video information coupled to said means for communicating information;

5 a means for displaying said image, said means for displaying said image coupled to said means for communicating information; and

a means for transmitting said sound, said means for transmitting said sound coupled to said means for communicating information.

10 8. The digital video computer system of Claim 7 wherein said video information is encapsulated in a communications packet compliant with Institute of Electrical and Electronics Engineers (IEEE) 1394 communication protocol standard.

15 9. The digital video computer system of Claim 8 wherein said video information is arranged in DIF blocks included a video frame.

10. The digital video computer system of Claim 9 wherein
a real time digital video data section of said communications packet carries a
20 constant number of DIF blocks and the first DIF block included in each of
sequential real time digital video data section falls within an integer number
of DIF blocks of one another, and the integer number is utilized to predict a

DIF block identification of the first DIF block expected in the next sequential communications packet.

11 The digital video computer system of Claim 9 wherein said the patch
5 task performed by said means for processing information to determine if said
video information is not received and providing patch video information
fills in appropriate default information for sequence number bits, a reserved
bit, DIF sequence number bits, DIF block number bits and section type bits if a
DV packet is missing, including values calculated to provided expected values
10 based upon ordering constraints of predetermined video information
configuration requirements.

12 A missing application data patching method comprising the steps of:
a) receiving a communication packet carrying application data;
15 b) separating application data from other communication packet protocol
data;
c) analyzing received application data to determine if it complies with
expected predefined data configuration;
d) providing a data patch for lost or missing application data; and
20 e) forwarding application data and patch data for further processing.

13 The missing application data patching method of Claim 12 wherein
said communications packet is compliant with IEEE 1394 communication
protocol standard.

5 14 The missing application data patching method of Claim 12 wherein
said patch data includes application default data.

15 A digital video information patching method comprising the steps of:

- 10 a) receiving a communication packet;
- b) separating digital video information from other communication
packet protocol data;
- c) determining if said communication packet includes a start of a digital
video frame;
- 15 d) analyzing if an appropriate starting DIF block is received first in said
communication packet;
- e) forwarding the received communication packet for further processing
if the section (SCT), DIF block number (DBN), and DIF sequence value (Dseq)
match expected values; and
- 20 f) performing a DIF patch if section type (SCT), a DIF sequence number
(Dseq) and a DIF block number (DBN) do not match expected values.

16 A digital video information patching method of Claim 15 wherein said communications packet is an IEEE standard 1394 compliant isochronous packet carrying digital video information.

5 17 A digital video information patching method of Claim 15 wherein step b) further comprises the step of removing a data length section, tag section, channel section, Tcode section, Sy section, a header CRC section and a data CRC section.

10 18 A digital video information patching method of Claim 15 wherein step c) further comprises the step of checking to ascertain if section type bits and sequence bits are logical zeroes.

15 19 A digital video information patching method of Claim 15 further comprising the step of retrieving a Quantization (QU) value from an audio auxiliary (AAUX) field in an A3 DIF block of sequence number zero if a received DIF sequence value equals zero, it is an audio section and a DIF block number is set to equal 0x03.

20 20 A digital video information patching method of Claim 15 wherein step d) further comprises the step of determining if information associated with a sequence value, a DIF sequence number and a DIF block number is sequentially continuous from a previously received communications packet.

ABSTRACT OF THE INVENTION

The present invention is a system and method that facilitates the reduction of presentation glitches in a digital video system. The present invention is a system and method that automatically determines if digital video (DV) data is missing from a stream of DV information and replaces or patches missing DV data with appropriate information to reduce the appearance of interruptions in the video (e.g., glitches in presentations). A communication packet carrying application data is received by an application data patching computer system . In one embodiment of the present invention, an IEEE standard 1394 compliant isochronous packet carrying digital video information is received. The application data (e.g., DV information) is separated from other communication packet protocol data (e.g., IEEE standard 1394 compliant header information). The received application data is analyzed to determine if it conforms to configuration constraints of predetermined application data format requirements. In one exemplary implementation, the initial information included in the application data section of the communication packet is analyzed to determine if it appropriately (e.g., sequentially) follows the information in previously received communication packet in accordance with predefined configuration definitions. If the information does not appropriately (e.g., sequentially) follow the information in previously received communication packet, a data patch is provided for lost or missing application data.

Data Block 151	Data Block 152	Data Block 153	Data Block 154	Data Block 155	Data Block 156	Data Block 157	Data Block 158	Data Block 159
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FIG 1A

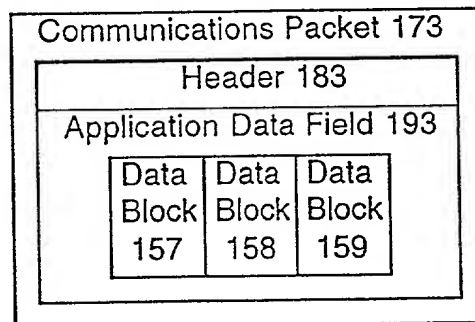
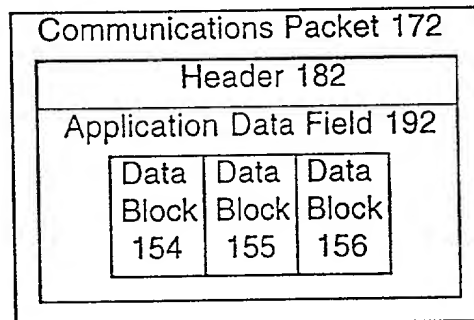
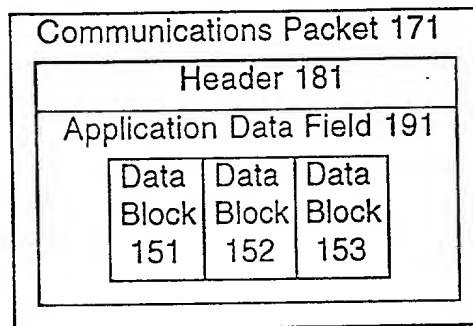


FIG 1B

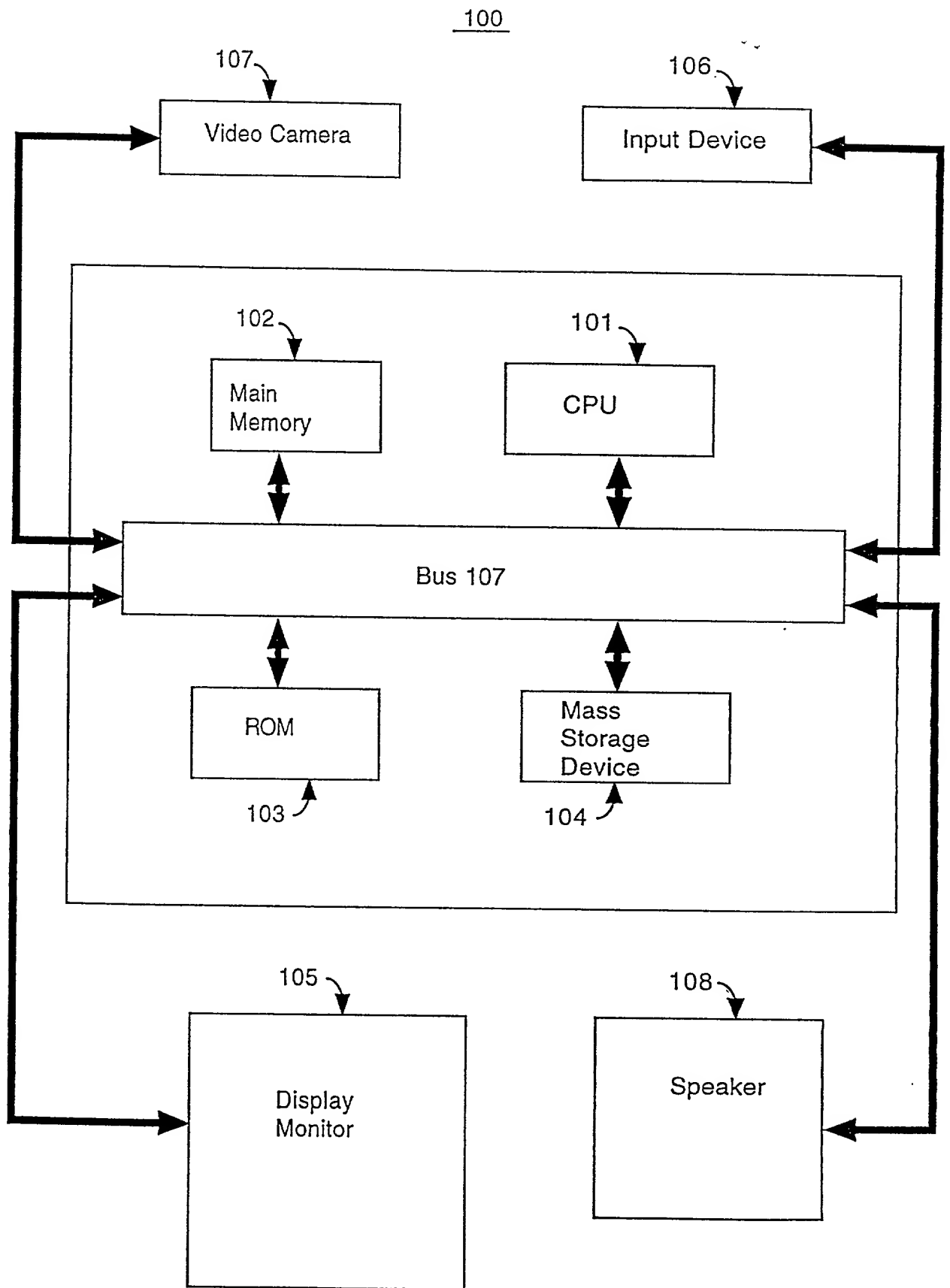


FIG. 1C

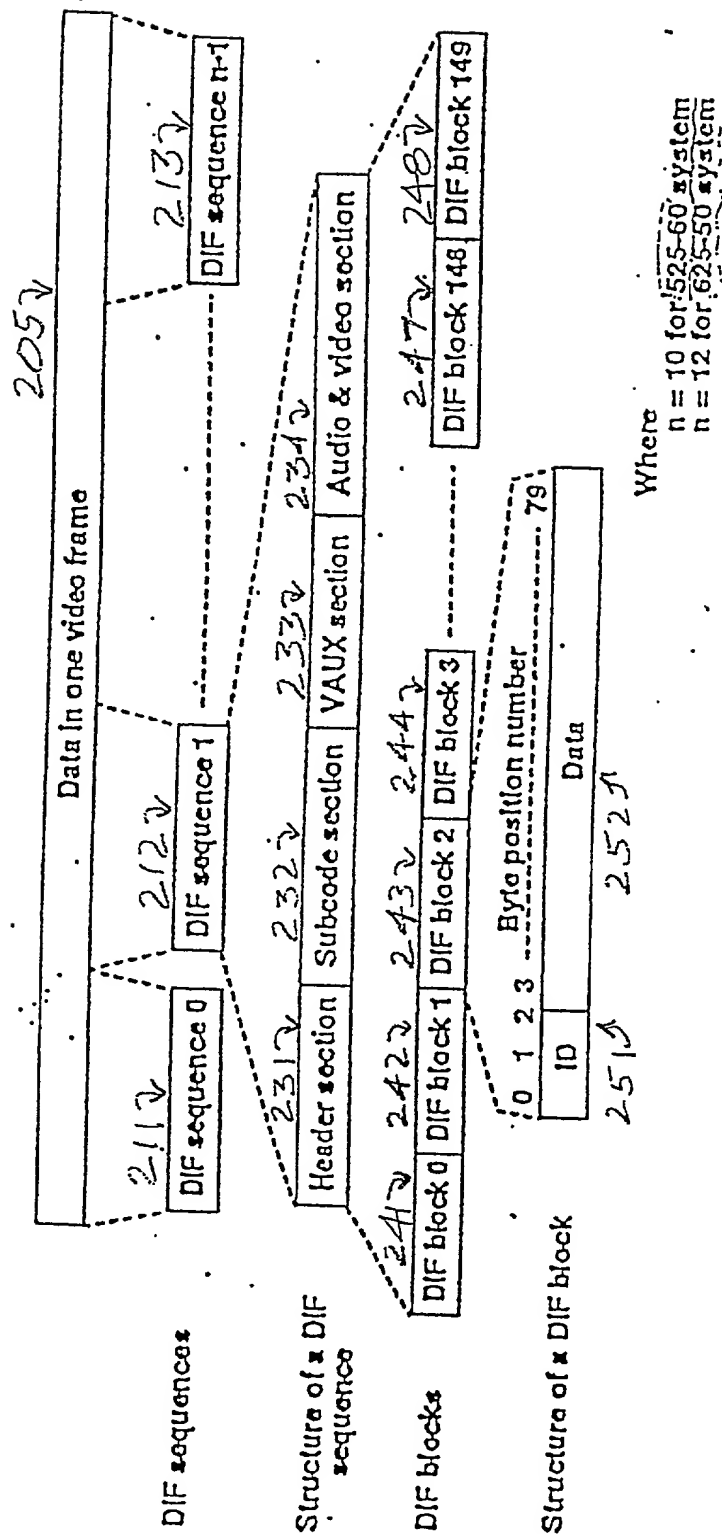
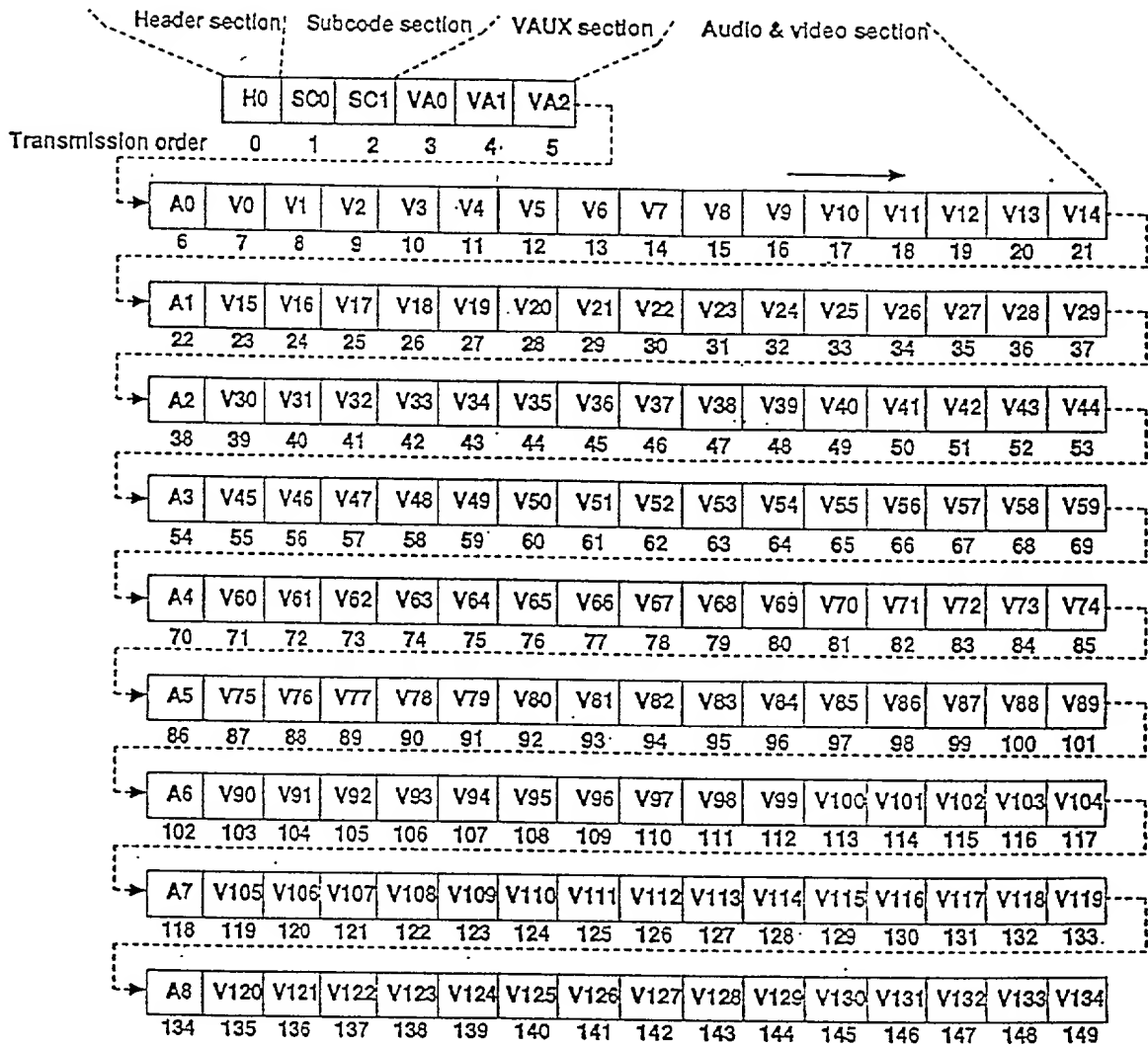


Figure. 2 -Data structure for transmission

300



DIF block

Where

- H0: DIF block in header section
- SC0 to SC1: DIF blocks in subcode section
- VA0 to VA2: DIF blocks in VAUX section
- A0 to A8: DIF blocks in audio section
- V0 to V134: DIF blocks in video section

Figure 3: - Transmission order of DIF blocks in a DIF sequence

	0 ID0	1 ID1	2 ID2
MSB	SCT ₂	Dseq ₃	DBN ₇
	SCT ₁	Dseq ₂	DBN ₆
	SCT ₀	Dseq ₁	DBN ₅
	RSV	Dseq ₀	DBN ₄
	Seq ₃	0	DBN ₃
	Seq ₂	RSV	DBN ₂
	Seq ₁	RSV	DBN ₁
LSB	Seq ₀	RSV	DBN ₀

Figure 4.

Table 1. - DIF block type

SCT ₂	SCT ₁	SCT ₀	Section type
0	0	0	Header
0	0	1	Subcode
0	1	0	VAUX
0	1	1	Audio
1	0	0	Video
1	0	1	Reserved
1	1	0	
1	1	1	

Figure 5.

Table 2, - DIF sequence number (525-60 system)

Dseq ₃	Dseq ₂	Dseq ₁	Dseq ₀	Meaning
0	0	0	0	DIF sequence 0
0	0	0	1	DIF sequence 1
0	0	1	0	DIF sequence 2
0	0	1	1	DIF sequence 3
0	1	0	0	DIF sequence 4
0	1	0	1	DIF sequence 5
0	1	1	0	DIF sequence 6
0	1	1	1	DIF sequence 7
1	0	0	0	DIF sequence 8
1	0	0	1	DIF sequence 9
1	0	1	0	Not used
1	0	1	1	Not used
1	1	0	0	Not used
1	1	0	1	Not used
1	1	1	0	Not used
1	1	1	1	Not used

FIG 6

Table 3 - DIF sequence number (625-50 system)

Dseq ₃	Dseq ₂	Dseq ₁	Dseq ₀	Meaning
0	0	0	0	DIF sequence 0
0	0	0	1	DIF sequence 1
0	0	1	0	DIF sequence 2
0	0	1	1	DIF sequence 3
0	1	0	0	DIF sequence 4
0	1	0	1	DIF sequence 5
0	1	1	0	DIF sequence 6
0	1	1	1	DIF sequence 7
1	0	0	0	DIF sequence 8
1	0	0	1	DIF sequence 9
1	0	1	0	DIF sequence 10
1	0	1	1	DIF sequence 11
1	1	0	0	Not used
1	1	0	1	Not used
1	1	1	0	Not used
1	1	1	1	Not used

FIG 7

800

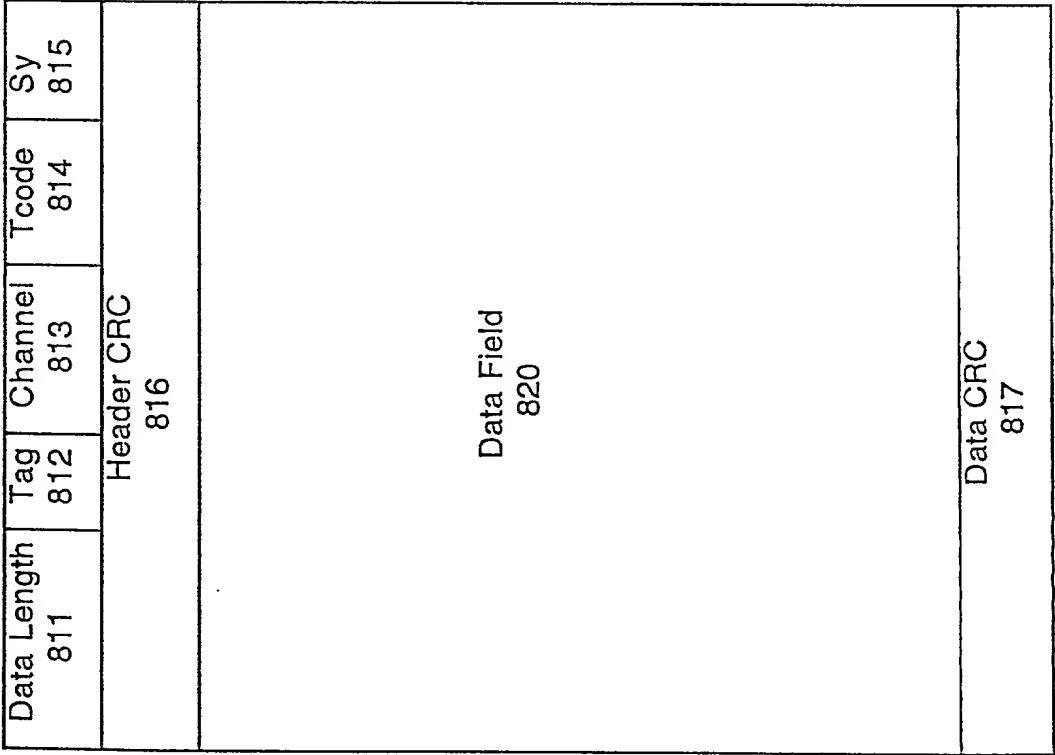


FIG 8A

820

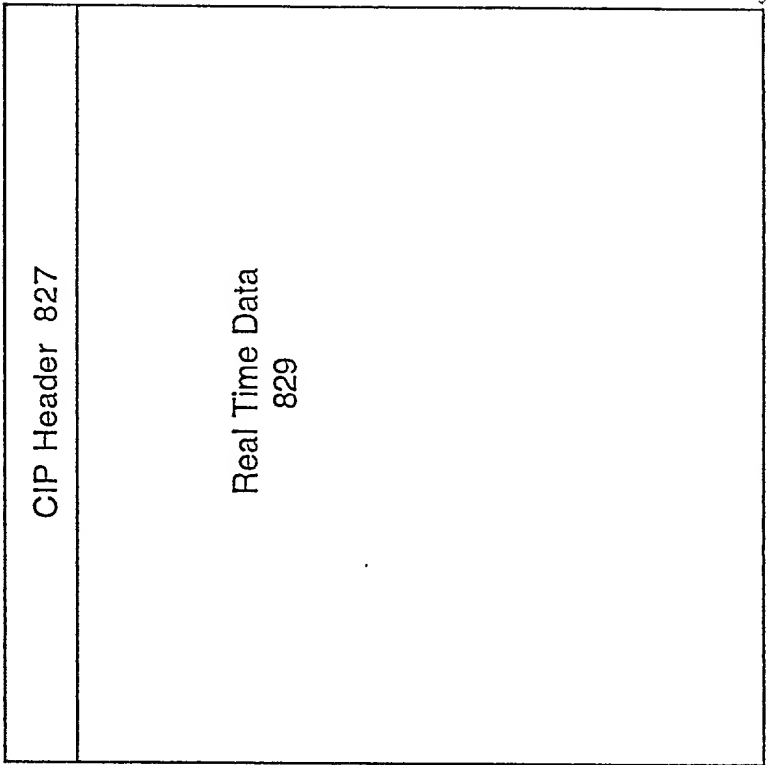


FIG 8B

900

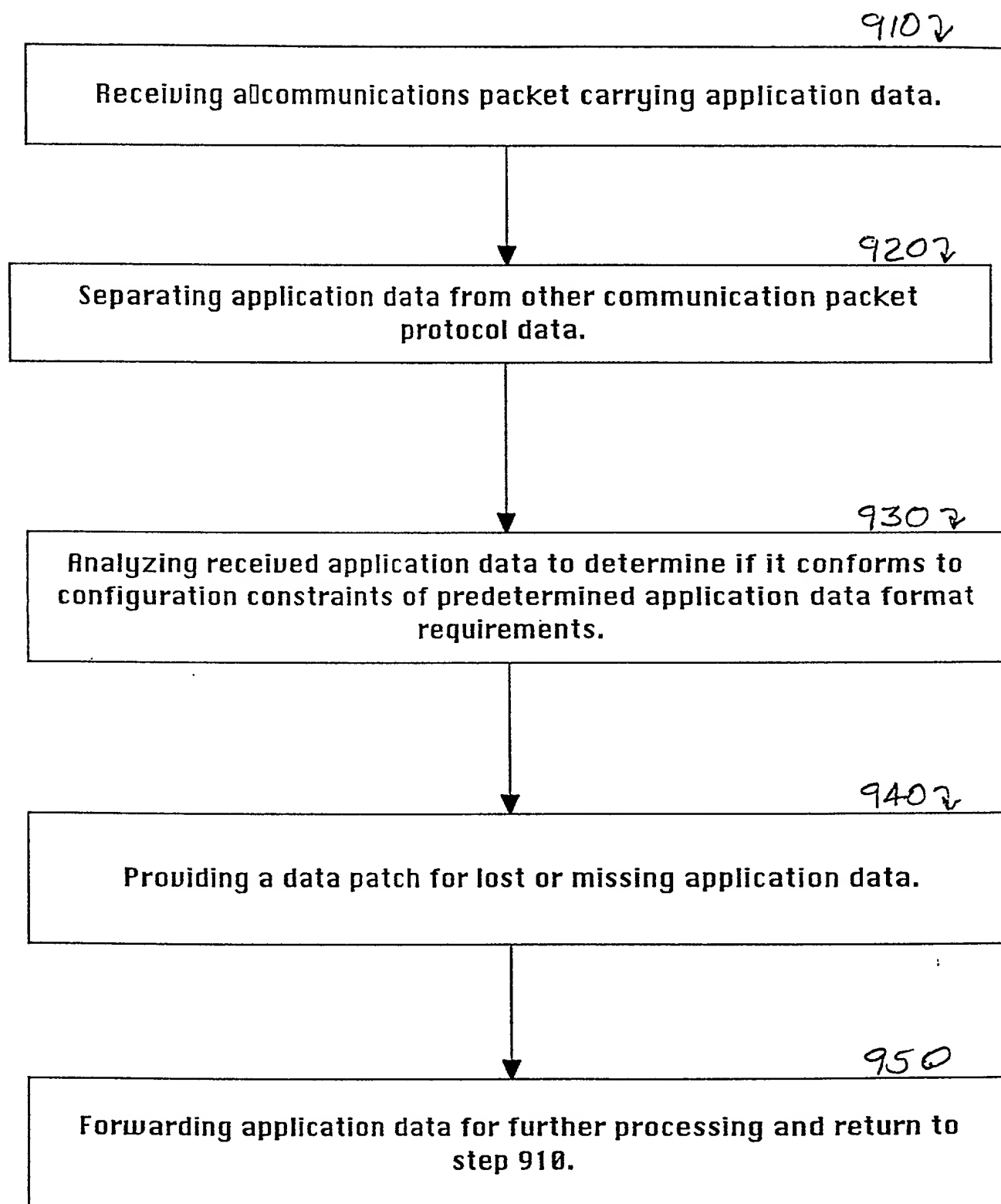


FIG 9

1000

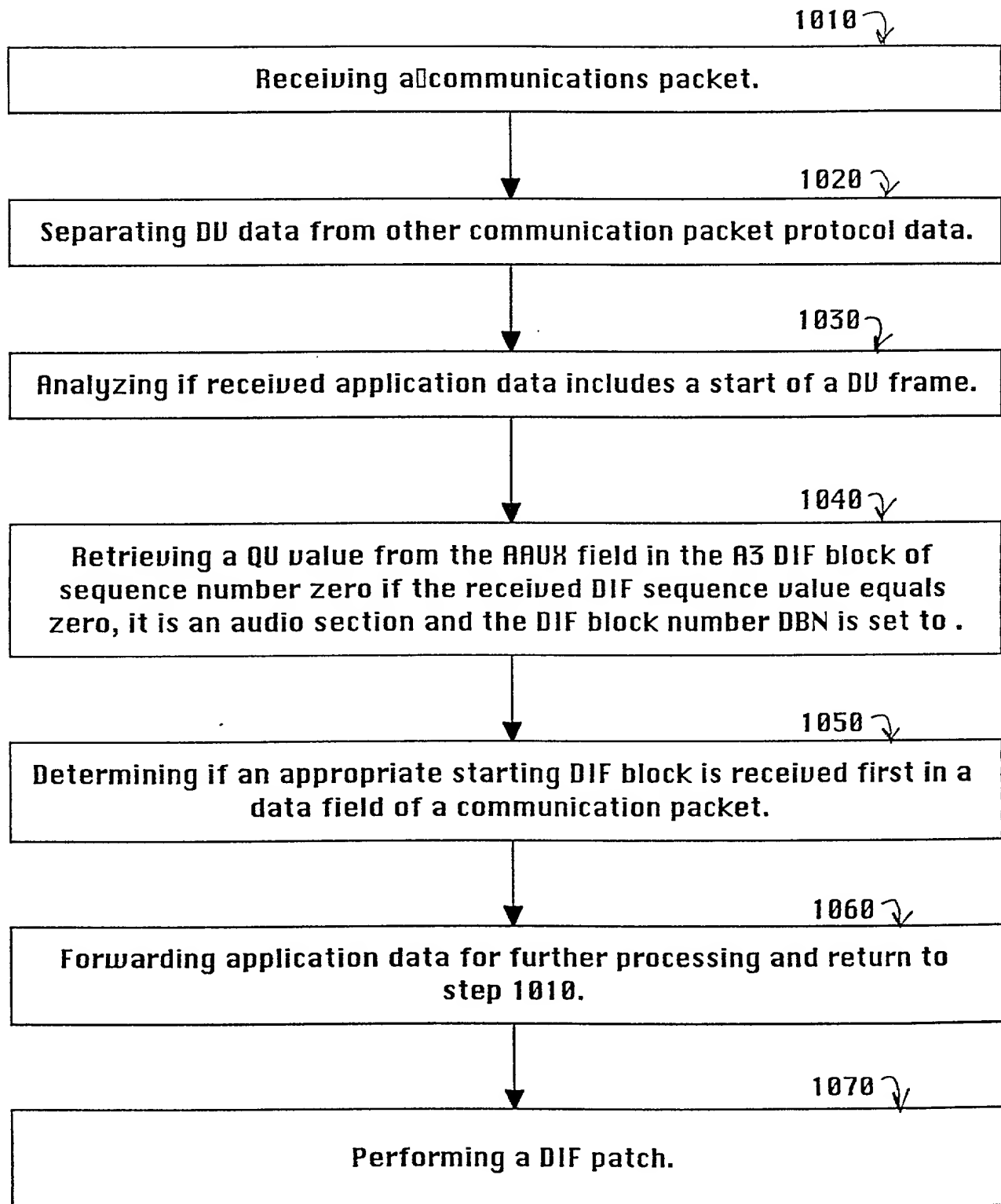


FIG 10

Declaration and Power of Attorney for a Patent Application

Declaration

As below named inventor, I hereby declare that my residence post office address, and citizenship are as stated below my name. Further, I hereby declare that I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

A SYSTEM AND METHOD OF PATCHING MISSING DIGITAL VIDEO PACKETS COMMUNICATED IN AN IEEE 1394 COMPLIANT IMPLEMENTATION

the specification of which:

☒ is attached hereto, or
 was filed on as application serial no. : and
 was amended on

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above; and

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

Foreign Priority Claim

I hereby claim foreign priority benefits under Title 35, United States Code Section 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Number	Country	Date Filed	Priority Claimed
.....	yes no
.....	yes no

U.S. Priority Claim

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, Section 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Serial Number	Filing Date	Status (patented/pending/abandoned)
.....
.....

Power of Attorney

As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent Trademark Office connected therewith.

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Signatures

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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